

Emissions Simulation for Power Electronics Printed Circuit Boards



Patrick DeRoy

Application Engineer

Patrick DeRoy completed his B.S. and M.S. degrees in Electrical and Computer Engineering from the University of Massachusetts Amherst in 2012. His coursework focused primarily on microwave and RF engineering, but he now specializes in the field of Electromagnetic Compatibility (EMC). He completed his Master's work with investigations on cable shielding and transfer impedance modeling using CST STUDIO SUITE and validating simulation results with measurements. He is an Application Engineer at CST of America, Framingham, MA, supporting customers modeling EMC problems including ESD, conducted and radiated emissions and BCI, among others. He is also interested in the simulation of PCBs for Signal and Power Integrity and mitigation of EMI at the board level.

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Agenda

Introduction and Motivation

Coupled 3D Field and Circuit Simulation

Conducted Emission of a Motor Control

• Variable frequency drive, the effect of PCB layout

Conducted Emission of a DC-DC Buck Converter

• Effect of EMI filter

Radiated Emission of a DC-DC Buck Converter

• RE as a near field coupling and the effect of shielding

Summary and Q&A



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EMC at Different Stages of the Design Process

Multiple troubleshooting iterations

High effort and costs to correct

Delayed time to market

















EMC Simulation in the Design Process

Design Stage

- Should be employed as early as possible in order to minimize the need for troubleshooting
- Can be performed without a prototype
- Can give answers to fundamental "what if" questions
- Can deliver outputs not accessible by measurements

Troubleshooting

- Can help to understand behavior of the device
- Not a competitor to measurements, both should be used complementarily



Power Electronics designers require a deep breadth of knowledge across many disciplines – circuit design, magnetics, semiconductor devices, thermal management, control theory, PCB layout, EMI...

EMI continues to be a major problem! Especially for Switched Mode Power Supply (SMPS) devices

Concepts well known, yet it can still be difficult to pass EMC regulations



PCB with CAN Interface, Noise Currents due to BCI on Power Wires

















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Coupled 3D Field and Circuit Simulation









Coupling is not only one way!







Coupled 3D Field and Circuit Simulation

Left: Buck Converter Data Sheet Schematic Right: Coupled 3D-Circuit Model Schematic





For more information...

http://emclive2014.com/technical-program-2/

Simulation of PCB Emissions October 15th 2014

www.cst.com/Events/Webinars

Filter for Archive

Simulation for EMC Performance in Modern Electronics November 6th 2014

EMC Simulation of a Motor Control September 24th 2015

Getting Ahead with Coupled 3D Field and Circuit Simulation February 18th 2016



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Variable Frequency Drive





Variable Frequency Drive

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Properties

- Energy efficiency
- Speed regulation
- Torque regulation
- Low maintenance effort

Automotive Application Areas

- Fuel pumps
- Motor cooling fans
- Electric engine













Prototypes with Different PCB Layout

Good Design

- Wide trace
- Balanced length





Bad Design

- Narrow trace
- Unbalanced length









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Port Placement

• All lumped components for filtering are replaced by ports















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Coupled Simulation Results

4 rotations Input Voltage coupled_motor [Real Part] C_in [Real Part] 13 MotU - C_in - MotV 6 - MotW 12 4 11 2 10 Voltage / V 0 -2 8 -4 7 -6 6 -8 5 0 0.005 0.01 0.015 0.02 0.025 0.03 0.035 0.04 0.01 0 0.005 0.015 0.02 0.025 0.03 0.035 0.04 Time / s Time / s

Current / A

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Coupled Simulation Results


Motor Control

Simulation Model and Comparison to Measurements

0.1 0.01 PCB A Measurement PCB_A_simulation 0.001 0.0001 1e-005 1e-006 1e-007 1e-008 1e-009 1e-010 0.8 1.2 1.4 0.4 0.6 Δ 0.2 1.6 1.8 2 Frequency / MHz













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Emission from Voltage Converter

Step down buck converter designed at CST









Model Discretization (Tetrahedral Mesh)



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Schematic

EMI filter enabled: Pins: SW1_2_3, SW4_1_2 -> shorted SW1_1_2, SW4_2_3 -> left open

EMI filter disabled: Pins: SW1_2_3, SW4_1_2 -> left open SW1_1_2, SW4_2_3 -> shorted

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Emission from Voltage Converter





sim vs. meas unfitered [Magnitude in dBu]



sim vs. meas filtered [Magnitude in dBu]





Emission from Voltage Converter

Electric field at 488 kHz











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EM Field Coupling from Buck Converter to AM/FM Antennas





AM Radio Reception Inside of a Car



Courtesy of Cyrous Rostamzadeh – Bosch, Plymouth, MI



DUT: DC-DC Buck Converter, Eval Board







150 kHz - 30 MHz, Unshielded Inductor L1, (Resolution Bandwidth 9 kHz, Average Detector)



Nearfield Probe Measurement at 1 MHz

×









Copper Shield over L1 and SW Node













150 kHz - 30 MHz, <u>Unshielded Inductor</u> L1 vs. <u>Shielded Inductor</u> L1 Comparison (Resolution Bandwidth 9 kHz, Average Detector)







Modeling for Further Investigation



Instrumental to answer "what-if" scenarios?

Exploit optimum SW Node "high dv/dt" trace area (parameterize geometry).

Extract parasitic inductances from PCB geometry.

Explore Shielding requirements "Shielding Effectiveness" for Compliance.



PCB Prototype vs. Model



Inductor Coil Design



The coil was designed so that it matches the specifications. Inductance is 15 uH.





Inductor Model, Magnetic Shield

We have placed a Mue=1000 material box around the inductor. This does clearly reduce the H field above the inductor (30dB), little effect on E Field (0.2dB).





Inductor Shielded with Metal Sheet

Adding a long shield above the inductor and the switch node does reduce the E and H field 1 cm above the PCB





Inductor Shielded with Metal Sheet

Adding a long shield above the inductor and the switch node does reduce the E and H field 1 cm above the PCB







H-Field, 1 MHz, Side View

Unshielded





H-Field, 1 MHz, Side View

Magnetically Shielded



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Unshielded, Ez 1cm above PCB





E-field, 1 MHz, Side View



No Shielding





Magnetic Shield over Inductor



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Electric Shield over Inductor only




Electric Shield over Inductor + SW Node





Near Field Coupling

Noise coupling phenomena below 30 MHz (deep in Near-Field Zone) with vehicle antenna is via E and H fields. E and H fields coexist at all times regardless of noise source impedance as seen here.



H-Field Shielded Inductor + using low inductance capacitor and practicing best EMI guidelines, i.e., reduction of mounting inductance is NOT sufficient. L1 and SW node trace <u>MUST be shielded using a conductive material (i.e.,</u> <u>copper) and bonded to PCB Ground</u>.



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Summary

Coupled 3D Field and Circuit Simulation has created new possibilities for EMC design

Can be very useful in the design for power electronics (motor control, converters, inverters, etc.) where EMI is becoming more prevalent

Conducted Emission of a Motor Control – PCB layout effects

Conducted Emission of a DC-DC Buck Converter – Filtering effects

Radiated Emission of a DC-DC Buck Converter – Shielding effects

Many types of mitigation techniques can be investigated and experimented with on virtual prototypes, long before testing occurs



Q&A

Thank you for your attention!

Any questions?



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Thanks for attending!

Don't miss our Test Bootcamp!

November 16, 2016 www.emclive2016.com